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An Advanced Load-Line Analysis Software for use in the Design and Simulation of Microwave Low-Distortion, High-Efficiency and High-Power GaN HEMT Amplifiers

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ABSTRACT: An advanced load-line analysis software is devised for nonlinear circuit design and simulation of microwave low-distortion, high-efficiency and high-power GaN HEMT amplifiers. A single software package can incorporate DC, small- and large-signal performances of GaN HEMT devices, and then analyze nonlinear performance of amplitude-to-amplitude (AM-AM) and amplitude-to-phase (AM-PM) modulations, and finally evaluate intermodulation distortion (IMD) and error vector measurement (EVM). High speed and high accurate simulation become available with the use of behavioral modeling for representing nonlinear performance of GaN HEMT devices. In addition, the software employs a time-domain analysis using time-varying electrical waveform and thus give clear and deep insight into the nonlinear behavior of GaN HEMT devices as well as the nonlinear circuit design technique of low-distortion and high-efficiency amplifiers. In comparison with the harmonic-balance (HB) method, comparable performances have been successfully achieved for an L-band 10W GaN HEMT amplifier.

KEYWORDS: Load-Line Analysis, Low-Distortion, High-Efficiency, Power Amplifier, Microwave, Nonlinear Circuit Analysis, GaN HEMT

1. Introduction

In recent years, low-distortion and high-efficiency of microwave high-power amplifiers represent one of the most crucial design issues in order to meet the stringent requirements of reduced cost and excellent thermal treatment of the modern wireless transmitting systems. As a starting point of power amplifier (PA) designs, the Cripps load-line theory [1] is widely used to know available output power and efficiency as well as load conditions. The Cripps load-line theory, however, has adopted the simplified device description and thus strong nonlinearity including hard saturation, large leakage current and low-frequency dispersion effects of GaN HEMT devices cannot be accurately described [2-3]. Therefore, the PA designs utilize the active and/or passive load-pull measurements as a following step to know the optimum load impedances under the actual operating conditions [4]. The load-pull measurements are, however, limited by frequency, power, impedance range, number of harmonics and stability [5]. Therefore, most of the PA

designs move to the nonlinear circuit simulations using harmonic-balance method [6]. The harmonic-balance method requires the accurate nonlinear device models. Indeed, the load-pull measurement and the harmonicbalance simulation are actually a powerful tool for PA designs but only a few information on the PA designs related to low-distortion and high-efficiency can be derived. On the other hand, the load-line theory is based on time-domain waveform analysis and thus provides much useful information on load and bias conditions for low-distortion and high-efficiency.

The author has presented the nonlinear load-line analysis method to demonstrate AM-AM and AM-PM characteristics of GaAs MESFET devices in 1995 [7] and in 2001 [8]. The method, however, cannot deal with strong nonlinearity such as hard saturation and large leakage currents. Moreover, the method is based on the measured data and thus time-consuming and inaccurate simulations were crucial design issues. In order to address these design issues, behavioral modeling is utilized to represent



nonlinearity of GaN HEMT devices. Moreover, the calculated AM-AM and AM-PM characteristics are represented by behavioral modeling. It makes available the 2-tone power series and envelope analyses including IP and IMD as well as EVM [9] evaluation of the modern wireless transmitting systems with high speed and high accuracy. The load-line analysis method presented here can be performed to run software written by MATLAB R2021b [10]. This is the first nonlinear load-line analysis software package ever reported. An L-band 10W GaN HEMT amplifier has been designed by using this software and compared with the harmonic-balance method [6] to make sure the validity of the software.

2. Advanced Techniques in Load-Line Analysis

2.1. Time-Varying Electrical Waveform Analysis

Principles of the load-line analysis is shown in Figure 1 [7]. Drain current $I_d(t)$ and drain voltage $V_d(t)$ swing on the load-line having a resistive slope of -gl within the area surrounded by Vk (Knee voltage), Vbr (breakdown voltage), Vbr+Vp (Vp is a pinchoff voltage) and zero. As a magnitude of Id(t), denoted as A(J), increases with input power, the upper or the lower-half of Id(t) is clipped by Idss or zero. That is, DC component of Id(t) expanded by Fourier series increases or decreases. It means that the initial bias point a (V_{do}, I_{do}) moves to a different bias point. For example, under class-AB or B operation, the lower half of Id(t) is clipped first. DC component increases and the bias point moves upward in conjunction with the loadline. Next the upper-half of Id(t) is clipped. DC component decreases and the bias point moves downward in conjunction with the load-line. This procedure is repeated until the bias point converges to some quiescent bias point b (Vdo, Idav).



Figure 1: Principles of the load-line analysis. Drain current I_d(t) and drain voltage V_d(t) swing on the load-line having a resistive slope of -gl within the area surrounded by V_k (Knee voltage), V_{br} (breakdown voltage), V_{br}+V_p (V_p is a pinchoff voltage) and zero. Point *a* is an initial bias condition (V_{do}, I_{do}). Point *b* is a final bias condition (V_{do}, I_{do}).

 $I_d(t)$, $V_d(t)$, a dynamic load-line are calculated by this load-line analysis software for GaN HEMT devices with

 V_k of 2V, V_{br} of 100V, V_p of -2V and Idss of 2.14A, which is shown in Figure 2(a). As A(J) increases from 0.2 to 2.2A, Id(t) and Vd(t) also increase and the bias point moves upward from the initial point (10V, 0.214A) in conjunction with the load-line. The slope of -gl can be varied as a dynamic load-line but keep constant in this case. Output power (Pout), drain efficiency (ηd), DC consumption power (Pdc) and Vd x Id can be calculated for a variation of A(J) and plotted in Figure 2(b). As A(J) increases, Pout goes up to 10W and ηd also increases.



Figure 2: (a) Calculated Id(t), Vd(t) and dynamic load-line. (b) Calculated Pout, ηd , Pdc and Vd x Id for GaN HEMT devices with Vk of 2V, Vbr of 100V, Vp of -2V and Idss of 2.14A

2.2. Large-Signal GaN HEMT Model Used in the Analysis

A large-signal GaN HEMT model is employed in the analysis, which is shown in Figure 3. Nonlinear circuit elements are transconductance (gm), drain-to-source resistance (Rds), gate-to-source capacitance (Cgs) and gate-to-drain capacitance (Cdg), which are obtained from I-V curves as a function of the gate voltage (Vg) and the drain voltage (Vd). A forward gate current (Igs) and a backward gate leakage current (Idg) are also included in the analysis for hard saturation and large leakage conditions. The nonlinear circuit elements (gm, Rds, Cgs, Cdg) and the gate current (Ids and Idg) are basically represented by behavioral modeling [11].



Figure 3: Large-signal GaN HEMT model. L_g, R_g, L_s, R_s, L_d and R_d are an extrinsic element, which are linear and thus keep a constant value. On the other hand, R_i, C_{ds}, C_{RF} and R_c are an intrinsic element, which are also linear and thus keep a fixed value. R_{ds} is used to represent DC characteristics. Therefore, RF characteristics are represented by $1/(1/R_{ds} + 1/R_c)$ for a large value of C_{RF}.

Nonlinear circuit elements of gm, R_{ds} , C_{gs} and C_{dg} are obtained from I-V curves as a function of V_g and V_d , which is shown in Figure 4. Since $I_d(t)$ moves on the load-line with A(J), gm and g_{ds} (=1/ R_{ds}) defined by (1) and (2) varies with A(J). Under large-signal operation, therefore, gm and



gds are represented as an averaged value for one period, which are given as gm_{ave} and g_{dsave} by (3) and (4) [8]. Id (V_{gr} , Vd) is represented by behavioral modeling in place of the measured data for high speed and high accurate calculation. The Curtice Cubic Model [12] is used here.

$$gm = \lim_{\Delta V_g \to 0} \frac{I_d(V_g + \Delta V_g, I_d) - I_d(V_g, I_d)}{\Delta V_g}$$
(1)

$$g_{ds} = \lim_{\Delta V_d \to 0} \frac{I_d (V_g, V_d + \Delta V_d) - I_d (V_g, V_d)}{\Delta V_d}$$
(2)

$$gm_{av} = \frac{1}{T} \int_0^T gm(t) dt = \frac{1}{N} \sum_{n=1}^N gm(n)$$
(3)

$$g_{dsav} = \frac{1}{T} \int_0^T g_{ds}(t) dt = \frac{1}{N} \sum_{n=1}^N g_{ds}(n)$$
(4)

 C_{gs} and C_{dg} defined by (5) and (6) also varies with A(J) on the load-line. Under large-signal operation, therefore, C_{gs} and C_{dg} are represented as an averaged value for one period, which are given as C_{gsave} and C_{dgave} by (7) and (8) [9-10]. In (5) and (6), C_{gs} and C_{ds} utilize the Statz model [13].

$$C_{gs} = C_{gs1} + \frac{C_{gs0}}{\left(1 - \frac{V_g}{V_{bi}}\right)^m} + C_{gs2}V_d$$
(5)

$$C_{dg} = C_{dg1} + \frac{C_{dg0}}{\left(1 - \frac{V_d - V_g}{V_{bi}}\right)^n}$$
(6)

$$C_{gsav} = \frac{1}{N} \sum_{l=1}^{N} C_{gs}(l) \tag{7}$$

$$C_{dgav} = \frac{1}{N} \sum_{l=1}^{N} C_{dg}(l)$$
(8)



Figure 4: Nonlinear circuit elements of gm, $R_{ds},\ C_{gs}$ and C_{dg} obtained from I-V curves as a function of V_g and V_d

DC, small-signal, large-signal circuit elements as well as nonlinear capacitances consisting of Figure 3 can be read from Microsoft Excel sheet, which are shown in Tables 1(a), 1(b), 1(c) and 1(d), respectively. With the use of these data, gm_{ave}, g_{dsave} C_{gsave} and C_{dgave} are calculated and plotted in Figure 5. It is clearly shown that nonlinear elements are drastically change with A(J).

 Table 1: DC, small-signal, large-signal circuit elements as well as nonlinear capacitances consisting of Figure 3





Figure 5: Calculated gm_{ave} , g_{dsave} C_{gsave} and C_{dgave} . Under class-B operation with a tuned load, $I_d(t)$ moves partly on the load-line with zero gm. Thus, the averaged gm sometimes decreases with A(J). However, the bias point moves upward and the averaged current also increases with A(J). Then gm drastically increases.



Figure 6: Large-signal S-parameters of GaN HEMT devices for A(J) from 0.2 to 2.2A at 1GHz.

Since gmave, gdsave, Cgsave and Cdgave are obtained for each A(J), S-parameters of Figure 3 can be calculated, which is shown in Figure 6. A calculation was done for A(J) from 0.2 to 2.2A at 1GHz. Amid these parameters, S₂₂ changes



remarkably. A variation of Mag(S₂₁) and Ang(S₂₁) leads to AM-AM and AM-PM performances. In conjunction with the data in Figure 2(b), the output power (P_{out}), power gain (G_P), drain efficiency (η_d), power-added efficiency (η_{add}) and insertion phase variation ($\Delta \phi$) are calculated and plotted in Figure 7.



Figure 7: Calculated output power (Pout), power gain (G_P), drain efficiency (η_{d}), power-added efficiency (η_{add}) and insertion phase variation ($\Delta \varphi$)

2.3. Behavioral Modeling

Once AM-AM and AM-PM performance are known, the distortion analyses including 2-tone power series analysis, 2-tone envelope analysis and EVM evaluation become available. Before the distortion analysis, AM-AM and AM-PM performances have to be represented by behavioral modeling for high speed and high accurate calculation. Behavioral modeling is listed in Table 2 [11]. The traditional distortion analysis of microwave power amplifiers deals with polynomial regression such as power series or Volterra series [1] because harmonic contents are easily handled. Thus, polynomial regression is employed here as behavioral modeling for representing AM-AM (Pout vs Pin) and AM-PM ($\Delta\phi$ vs Pin) performances shown in Figure 8.

Table 2: List of behavioral modeling: Behavioral modeling includes regression analysis and curve-fitting technique

Regression Analysis	Curve-Fitting Technique	
Linear regression	Asymtpotes slope	
Logarithmic regression	Left hand technique	
Power function regression	Right hand technique	
Exponential regression	Build curve-fit function	
Polynomial regression	Taylor's expansion	
	Spline curve-fit technique	



Figure 8: AM-AM and AM-PM performances of microwave power amplifiers. ΔG is a compressed gain and $\Delta \phi$ is an insertion phase variation, that is, a phase distortion

Based on the AM-AM (Pout vs Pin) and AM-PM ($\Delta \phi$ vs Pin) performances of Figure 7, the 3rd polynomial equations are calculated, which are shown in (9) and (10). Pin and Pout are denoted as antilog value. $\Delta \phi$ is given as degree.

$$P_{out} = a_3 P_{in}^3 + a_2 P_{in}^2 + a_1 P_{in} + a_0$$
(9)

$$a_3 = -5.8643E3$$

$$a_2 = 0.5233E3$$

$$a_1 = 0.0946E3$$

$$a_0 = 0$$

$$\Delta \phi = a_3 P_{in}^3 + a_2 P_{in}^2 + a_1 P_{in} + a_0 \quad (10)$$

$$a_3 = -3.1400E3$$

$$a_2 = 1.2512E3$$

$$a_1 = -0.1019E3$$

$$a_0 = -0.0002$$

The calculated AM-AM and AM-PM performances shown in Figure 7 are also demonstrated in Figure 9 in conjunction with behavioral modeling. A good agreement has been achieved between the calculated and modeled data.



Figure 9: Calculated AM-AM and AM-PM performances combined with behavioral modeling. (a) AM-AM performance at 1GHz. (b) AM-PM performance at 1GHz. Pout and Pin are antilog number. $\Delta\phi$ is represented as degree

2.4. Distortion Analysis (2-tone Analysis)

This load-line analysis software prepares two types of 2-tone analyses: 2-tone power series analysis for weak nonlinearity and 2-tone envelope analysis for strong nonlinearity [1]. For example, in the 3^{rd} -order 2-tone power series analysis, 2-tone signal described in (11) and (12) is inserted into (9). Then the 2^{nd} -degree term of (9) produces the 2^{nd} -order product at $2\omega_1$, $2\omega_2$, $\omega_1\pm\omega_2$. The 3^{rd} -degree term provides the 1^{st} - and 3^{rd} -order products at ω_1 , ω_2 , $3\omega_1$, $3\omega_2$, $2\omega_1-\omega_2$, $2\omega_2-\omega_1$. The 1^{st} -, 2^{nd} - and 3^{rd} -order products are calculated and plotted as P_{in}-P_{out} in Figure 10. IIP₃ can be easily obtained from the intersection point of an extended linear part of ω_1 and an extended linear part of ω_3 .

$$P_{in} = v_1 \cos \omega_1 t + v_2 \cos \omega_2 t \tag{11}$$

$$v_1 = v_2 = v \tag{12}$$

The 2-tone envelope analysis is shown in Figure 11 [1]. An envelope of the input 2-tone signal is modulated by a difference frequency of $\omega_1 - \omega_2$ ($\omega_1 > \omega_2$). The amplified



output signal is distorted in both magnitude and phase through AM-AM and AM-PM performances of PAs, which produces a serious intermodulation distortion. Input and output signals are given by (13) and (14). The input time-domain signal g(m) can be transformed from the frequency-domain signal G(k) by the inverse Fourier transformation as (15). The input signal is amplified and then the time-domain output signal g'(m) is given by (16). Finally, the frequency-domain output signal G'(k) is transformed by Fourier transformation as (17).



Figure 10: The 1st-, 2nd- and 3rd-order products. Red curve is the 3rd-order product ($2\omega_1-\omega_2$ or $2\omega_2-\omega_1$), which appear in close to carrier frequencies of ω_1 and ω_2 .

$$V_i(t) = Re|\rho \cdot \exp(j\omega t)|$$
(13)

$$V_o(t) = \frac{Re}{A}(|\rho|) \cdot \exp(j\omega t + j\theta(|\rho|))|$$
(14)

$$g(m) = \sum_{k=0}^{\infty} G(k) exp\left(\frac{i2\pi mk}{N}\right)$$
(15)

$$g'(m) = |A(|g(m)|) \cdot \exp(j\theta(|g(m)|))| \quad (16)$$

$$G'(n) = \frac{1}{N} \sum_{k=0}^{N-1} g'(k) exp\left(-\frac{i2\pi nk}{N}\right)$$
(17)



Figure 11: 2-tone envelope analysis. An envelope of input signal has a sinusoidal waveform beat by a difference frequency. An envelope of the amplified output signal is distorted in both amplitude and phase.

Time- and frequency-domain output signals are calculated with the use of 2-tone envelope analysis shown in Figure 11 and behavioral modeling of Figure 9 for 2-tone signal (f_1 =0.9GHz, f_2 =1.1GHz, v=0.02V) in (11) and

(12), which are displayed in Figure 12. Figure 12(a) shows a time-domain output signal and Figure 12(b) displays a frequency-domain output signal (spectrum). IMD₃ signals ($2f_1$ - f_2 and $2f_2$ - f_1) appear adjacent to carrier signals (f_1 and f_2). In addition, a difference signal (f_2 - f_1), a sum signal (f_1 + f_2), 2^{nd} -harmonic signals ($2f_1$ and $2f_2$) are also clearly shown. Due to the maximum limit of memory size of the computer, the resolution of spectrum becomes poor.



Figure 12: 2-tone envelope analysis: Time- and frequency-domain output signals are calculated for 2-tone signal (f_1 =0.9GHz, f_2 =1.1GHz, v=0.02V)

2.5. EVM Evaluation

Error vector magnitude (EVM) evaluation can provide a great deal of insight into the performances of digital communications transmitters and receivers [14]. The error vector is defined as a vector difference at a given time between the ideal reference signal and the measured signal, which is shown in Figure 13. AM-AM performance having ΔG and AM-PM performance having $\Delta \phi$ in Figure 13(a) produce a serious vector error in Figure 13(b).



Figure 13: Error vector magnitude. (a) AM-AM and AM-PM data for use in the analysis. (b) Description on EVM schemes.

Now EVM is evaluated for GaN HEMT amplifiers having AM-AM and AM-PM performance shown in Figures 7, 9 and 13(a). EVM can be obtained by using MATLAB Simulink of EVM and MER measurement [15]. The explore model is used with an amplitude imbalance of 1 dB, a phase imbalance of 15 degrees and the DC offset of zero. Since the calculated AM-AM and AM-PM performances shown in Figures 7 and 9 cannot be used in the present form, the AM-AM and AM-PM data shown in Figure 9 are converted to a lookup table form. In the EVM analysis, 16-QAM modulated signal is used. S/N is assumed to be 40dB. EVM is evaluated at P_{in} of 5dBm for linear operation and 20dBm for nonlinear operation. Gain is 20dB at P_{in} of 5dBm and 17dB at P_{in} of 20dBm. The root-



mean-square, maximum and peak values of EVM are listed in Table 3 and the constellation is demonstrated in Figure 14. RMS value of EVM at P_{in} of 5dBm is much smaller than that of P_{in} of 20dBm, which means that a communication quality is higher because of low distortion conditions.

Table 3. Calculated root-mean-square (rmsEVM), maximum (maxEVM) and peak (pctEVM) values of EVM



Figure 14: Constellation diagram of 16-QAM modulated signal. Red and blue dots are ideal and distorted signals at $P_{\rm in}$ of 20dBm. Red and yellow dots are ideal and distorted signals at $P_{\rm in}$ of 5dBm. It can be clearly shown that the constellation is seriously distorted at Pin of 20dBm (blue dots).



Figure 15: Comparison of the simulated power performances using the harmonic-balance simulator (ADS2021) and this load-line analysis software

3. Comparison with Harmonic Balance Method

An L-Band 10W GaN HEMT amplifier using Cree GaN HEMT CGH40010 [16] has been designed. Power performances are compared by using the harmonicbalance simulator (ADS2022 Keysight Technology) [17] and this load-line analysis software, which is shown in Figure 15. Output power and gain are in good agreement. Power-added efficiency and insertion phase variation are slightly different. These results demonstrate that the loadline analysis software introduced here is a candidate for the nonlinear analysis of GaN HEMT amplifiers. To verify the validity of the load-line software, the L-band 10W GaN HEMT amplifier is due to be actually fabricated and measured hereafter.

4. Comparative Analysis

A comparative analysis of the load-line method used in the microwave power amplifier is summarized in Table 4. A low-frequency I-V load-line measurement setup is shown in [2] and [3] to analyze low-frequency dispersion phenomena of GaN HEMT devices. The Cripps load-line theory is slightly modified to meet with low-voltage devices such as CMOS in [18] and [19]. That is, a slope of the load-line is adjusted for high efficiency in accordance with the knee voltage. By tilting a slope of the load-line for each cell of the distributed amplifier, high power and high efficiency over several octaves have been obtained [20] and [21]. The load-line is carefully chosen to achieve low-distortion and high-efficiency for both carrier and peaking amplifiers of the Doherty amplifier [22] and [23]. It must be noted that not only the load-line is carefully investigated but also time-varying waveform is checked in these load-line analyses. Similar to [2] and [3], a lowfrequency I-V load-line is used to evaluate performance degradation of microwave transistor [24]. In addition, dynamic load-line is used in the design of narrowband and broadband amplifier designs [25] and [26]. This work presented here is based on a load-line analysis software, which can provide linear/nonlinear power and distortion performances. Therefore, this software can be considered to be useful to analyze various nonlinear power performance described in these References.

Table 4: Comparative analysis of the load-line method for use in the power amplifier design

Ref. No.	Year	Device	Load-line Method	Objective	Model
[2]	2009	800µm GaN	Low-frequency I-V load-line	Analysis of low-frequency dispersion (i.e., trans and thermal offects)	Generic nonlinear equivalent-
[3]	2014	0.25 600 m GaN HEMT	Low-frequency I-V load-line measurement (2MHz)	Analysis of low-frequency dispersion	Behavioral Modeling of current generator
[18]	2018	GaN HEMT CGH160015D	Pedro load-line method	Impact of knee voltage effect and soft turn-on characteristic on the design of Class-B/J power amplifiers	Not described
[19]	2013	sub-micron CMOS	Extension of the load line theory to higher knee voltage value	Investigating the impact of the Knee- voltage on output-power and efficiency	Not described
[20]	2014	0.25 μm Al- GaN/GaN	Tilting load-lines	Design of uniform distributed power amplifiers having broadband high power	Not described
[21]	1987	200 µm GaAs FET	Emplying different load-line for each cell of distributed amplifier	Broadband high power distributed amplifier	Smallsignal model
[22]	2008	Eudyna EGN010MK GaN HEMTs	Modulated load-line analysis	Analysis of saturated Doherty amplifier based on class-F amplifiers to maximize efficiency	OKI 0.1-W KGF1284 MESFET model
[23]	2008	Filtronic GaAs HEMT FPD750	Intrinsic load line of carrier and peak amplifier	WiMAX at 3.5 GHz is realized using a class AB amplifier	In-house Angelov non-linear model.
[24]	2021	Microwave Transistor	Low-frequency I-V and time- domain load-line measurement	Evaluation of microwave transistor degradation	Not described
[25]	2022	140nm GaN HEMT	Time-domain waveform analysis and dynamic load-line simulation	Accurate simulation of load-line and harmonic-balance methods	ASM-HEMT model
[26]	2023	GaN HEMT CGH40025F	Load-line analysis based on the series of continuum modes operation	Broadband amplifier design using class BJF-1	Not described
This Work	2023	Cree GaN HEMT CGH40010	Advanced load-line analysis for hard saturation large leakage current	Development of nonlinear load-line analysis software	Behavioral modeling

5. Conclusion

An advanced load-line analysis software for nonlinear circuit design and simulation of microwave lowdistortion, high-efficiency and high-power GaN HEMT amplifiers has been presented. A single software package can incorporate DC, small-signal and large-signal performances of GaN HEMT devices, and then analyze nonlinear performance of AM-AM and AM-PM characteristics, and finally evaluate IMD and EVM. With the use of behavioral modeling, high speed and high



accurate simulation become available. In addition, the software is based on a time-domain analysis using timevarying electrical waveform and thus can provide clear and deep insight into the nonlinear behavior of GaN HEMTs as well as the nonlinear circuit design of lowdistortion and high-efficiency GaN HEMT amplifiers.

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